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Application No.: 10/828,408
Filing Date: April 19, 2004
Inventor: LAKSHMANAN, Viswanathan
Art Unit: 2825
Confirmation No.: 4517
Examiner: Parihar, Suchin

From: Eric J. Whitesell #38657

Today's Date: July 17, 2006
Attorney Docket No.: 03-2644 81693
Pages: 7 pages (including this 1-page coversheet)
Dispatched by: Eric J. Whitesell

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Transmittal (1 pg.)

Response to Notice of Non-compliant Amendment (5 pgs.)

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/828,408	
	Filing Date	April 19, 2004	
	First Named Inventor	LAKSHMANAN, Viswanathan	
	Art Unit	2825	
	Examiner Name	Parihar, Suchin	
Total Number of Pages in This Submission	7	Attorney Docket Number	03-2644 81693

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Response to Notice page 1 of 5
10/828,408

DOCKET NO. 03-2644 81693

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): LAKSHMANAN,
Viswanathan et al.
Serial No.: 10/828,408
Filed: April 19, 2004
For: METHOD AND COMPUTER
PROGRAM FOR VERIFYING AN
INCREMENTAL CHANGE TO AN
INTEGRATED CIRCUIT DESIGN
Art Unit: 2825
Examiner: Parihar, Suchin

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July 17, 2006 / Eric James Whitesell /
Eric J. Whitesell #38657

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